

PAT-NO: JP360137158A  
DOCUMENT-IDENTIFIER: JP 60137158 A  
TITLE: CODE ERROR CORRECTING CIRCUIT  
PUBN-DATE: July 20, 1985

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NEC CORP N/A

APPL-NO: JP58249178  
APPL-DATE: December 26, 1983

INT-CL (IPC): H04L025/497  
US-CL-CURRENT: 714/746

ABSTRACT:

PURPOSE: To perform stable data transmission by detecting an error occurring to a signal after violation operation at a transmission side in the middle of transmission, and correcting the error.

CONSTITUTION: The signal C<SB>n</SB>' after violetion operation at the transmission side is cumulated by the cumulative calculating circuit consisting of an adding circuit 11, correcting circuit 12 and two-time-slot TS delay circuit 13. Further, the signal obtained by passing the signal C<SB>n</SB>' through a 2TS delay circuit 15, the signal generated by passing the cumulative calculated value through a 2TS delay circuit 14, and the signal C<SB>n</SB>'

are inputted to pattern detecting circuits 16~23 which detect a specific pattern of the code sequence of the signal  $C_{n-1}$ . When the circuits 16~19 and circuits 20~23 detect the specific pattern, their outputs are inputted to OR circuits 24 and 25 respectively. The output of the circuit 24 is ORed by an OR circuit 27 with the output of the circuit 25 through a 2TS delay circuit 26, and the output is inputted to an error code correcting circuit 28. The circuit 28 corrects the error of the output signal  $C_{n-2}$  of the circuit 15 into the output signal of the circuit 27 and outputs a signal  $C_{n-2}$ .

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